

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY**

Examination Centre: Main Building UCE (A), O.U.
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M. E. (ECE & Mech.) II - Semester (Make-up) Examination October 2010

VASAVI COLLEGE OF ENGG.

EXAMINATION TIME-TABLE

Time : 2.00 PM to 5.00 PM

“Centre : University College of Engineering, Osmania University, Hyderabad – 7”

Date and Day	E.C.E. (Embedded Systems & VLSI Design)	MECHANICAL (Advanced Design and Manufacturing)
	Regular II – Semester	Regular II – Semester
25-10-2010 MONDAY	Principles of VLSI System Design	Metal Processing Science
27-10-2010 WEDNESDAY	VLSI Physical Design	Computer Aided Mechanical Design and Analysis
29-10-2010 FRIDAY	Design for Testability	Advanced Non-Destructive Evaluation Techniques
01-11-2010 MONDAY	Low Power VLSI Design	Optimization Techniques
03-11-2010 WEDNESDAY	CPLD & FPGA Architectures and Applications	Robotic Engineering
08-11-2010 MONDAY	=====	Mechanics of Composite Materials
10-11-2010 WEDNESDAY	Real Time Operating Systems for Embedded Systems	=====

Note: This Time-Table is also applicable for Pre-Ph.D. students registered for July 2010 Examination.

DIRECTOR OF EVALUATION
EXAMINATION CELL